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Docket: 0756-1614

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Takeshi FUKUNAGA et al. )  
Serial No. 08/781,920 ) Art Unit: 1112  
Filed: December 30, 1996 ) Examiner: M. Padgett  
For: METHOD OF FABRICATING )  
A SEMICONDUCTOR DEVICE )  
UTILIZING A CATALYST )  
MATERIAL SOLUTION ) Date: September 23, 1998

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents

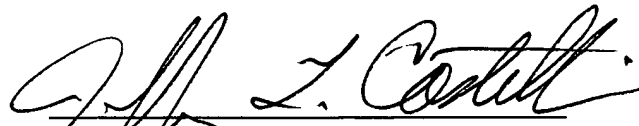
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Respectfully submitted,

  
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